

Appl. No. : 09/308,032  
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**AMENDMENTS TO THE CLAIMS**

**Please cancel claim 10 without prejudice, as indicated below.**

**Please amend claim 3 as indicated below.**

A complete listing of all claims is presented below with insertions underlined (e.g., insertion), and deletions struckthrough or in double brackets (e.g., ~~deletion~~ or [[~~deletion~~]]):

1. (Cancelled)

2. (Cancelled)

3. (Currently Amended) An image sensor comprising an array of columns and rows of pixels ( $X_{ij}$ ), all the pixels of one column of the array being connected to at least one common pixel output line ( $l_j$ ) having at least one memory element ( $M_j$ ) and at least one column amplifying element ( $A_j$ ), all said column amplifying elements ( $A_j$ ) being connected to a common output amplifier (D), each common pixel output line ( $l_j$ ) being divided through switches ( $S_4j$  and  $S_5j$ ) into at least two parallel circuits before the respective column amplifying element ( $A_j$ ), at least one of these parallel circuits having said memory element ( $M_j$ ), the two parallel circuits being connected through a switch ( $S_6j$ ) with the same input of said column amplifying element ( $A_j$ ), wherein there is a further switch ( $X_j$ ) between said column amplifying element ( $A_j$ ) and the common output amplifier (D), wherein said column amplifying elements ( $A_j$ ) and the common output amplifier (D) are connected by a single bus and wherein the image sensor is a CMOS or MOS device.

4. (Original) An image sensor as recited in claim 3, wherein both circuits have a memory element ( $M_{sj}$  and  $M_{rj}$ ).

5.-10. (Cancelled)

11. (Previously Presented) An image sensor according to claim 3, wherein the image sensor has at least two input terminals.